

JPW

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. OKI.561
In Re Application Of: Koichi FUKUDA			
Serial No. 10/635,006	Filing Date 08/06/2003	Examiner TBA	Group Art Unit 2811
Title: FULLY DEPLETED SOI-MOS TRANSISTOR			
<div style="display: flex; justify-content: space-between; align-items: center;"><div style="border: 1px solid black; border-radius: 50%; padding: 10px; text-align: center; width: 150px;">PTO MAY 27 2004 PATENT & TRADEMARK OFFICE</div><div style="text-align: center;"><small>Address to:</small> Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</div></div> <div style="text-align: center; margin-top: 10px;">37 CFR 1.97(b)</div> <div>1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</div> <div style="text-align: center; margin-top: 20px;">37 CFR 1.97(c)</div> <div>2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</div> <div style="margin-left: 40px; margin-top: 10px;"><input type="checkbox"/> the statement specified in 37 CFR 1.97(e);</div> <div style="text-align: center; margin-top: 10px;">OR</div> <div style="margin-left: 40px; margin-top: 10px;"><input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).</div>			

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In Re Application: **Koichi FUKUDA**

Serial No.
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TBA

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2811

FULL DEPLETION SOI-MOS TRANSISTOR



Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
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Dated: **MAY 27, 2004**

ADAM C. VOLENTINE
REG. NO. 33289

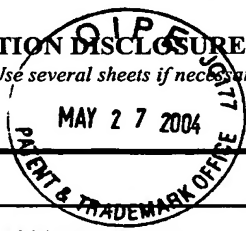
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CC:

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

OKI.561

Application Number

10/635,006

Applicant(s)

Koichi FUKUDA

Filing Date

08/06/2003

Group Art Unit

2811

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
	A	2001-332730	11/30/2001	JAPAN			✓	
	B	2000-183355	06/30/2000	JAPAN			✓	
	C	09-199610	07/31/1997	JAPAN			✓	
	D	07-245396	09/19/1995	JAPAN			✓	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.